

PATENT
Customer No. 22,852
Attorney Docket No. 08038.0027

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hikaru YOSHITAKA

Serial No.: 09/658,861

Filed: September 8, 2000

For: SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING
THE SAME

Group Art Unit: 2812

Examiner: Nguyen, H.

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT

In reply to the Office Action dated August 15, 2001, the period for reply having been extended for three months by a request for extension and fee payment filed concurrently herewith, please amend the application as follows:

IN THE CLAIMS:

Please cancel claims 1-3 and amend claim 4 as follows:

4. (Amended) A method of manufacturing a semiconductor device, said method comprising the steps of:

forming a wiring layer on a semiconductor substrate having an active region formed thereon;

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Varrell